



Department of Electronics and Communication Engineering
Indian Institute of Information Technology Allahabad
Allahabad 211012 (UP) India

Advt. No. 01/ECE/2016

Dated - 19/07/2016

Advertisement for the Post of Junior Research Fellow (JRF) (On Contract)

Applications are invited for the post of Junior Research Fellow (JRF) (on contract) in research project entitled “Silicon Nanowires: Professional Compact Model of Scaled Thermoelectric Figure of Merit for Energy Harvesting” in reference to the sanction letter no. File No.YSS/2015/000905 dated: 01.12.2015 by Department of Science & Technology, NewDelhi-110016. The duly completed application on prescribed format along with copies of supporting documents must reach to **Dr. RekhaVerma, Department of Electronics & Communication Engineering, Room No. 2106, Computer Centre – 1, Indian Institute of Information Technology Allahabad, Devghat, Jhalwa, Allahabad, Uttar Pradesh 211012 on or before 10/08/2016**. The envelope should be super scribed as “**Application for the post of Junior Research Fellow (JRF) (on contract) in the Research Project “Silicon Nanowires: Professional Compact Model of Scaled Thermoelectric Figure of Merit for Energy Harvesting”**”. A soft copy of the application should also be sent to r.verma@iiita.ac.in (PI of the project). The position is purely temporary and will be governed by the Funding agency rules & service conditions of Research & Development Cell, IITA Allahabad.

Name of Position : Junior Research Fellow (JRF)

Number of Position : 01 (one)

Project Duration : Appointment will be made on contract basis for a period of one year.

Essential Qualifications: M.Sc. Physics (specialization in Electronics) with one year experience/M.E./M.Tech. in Electronics with specialization in Microelectronics/VLSI Design or equivalent.

Desirable Qualifications: (i) A high GATE/NET/JEST or similar other national level ranking.

(ii) Good command in solid state physics/semiconductor devices/device modeling and quantum mechanics.

(iii) Knowledge in MATLAB programming and working knowledge of UNIX platform/server handling.

Fellowship: 25,000/- pm (Fixed).

Note:

1. The applicant will be responsible for the authenticity of information, other documents and photographs submitted.

2. The Institute reserves the right to accept application at any time, and consider candidates of exceptional

credentials without applications. Qualification and experience may be relaxed by the Institute at any point of time for otherwise exceptional candidates.

3. Mere, possessing the prescribed qualification does not ensure that the candidate would be called for Interview. The Candidates will be shortlisted on the basis of merit and need of the project.

4. Shortlisted Candidates will be informed by e-mail about the interview date. So, the candidate must provide valid E-mail IDs in their applications.

5. Shortlisted candidates have to present themselves for the interview on the interview date with updated CV and original and attested photocopies of mark sheets/ certificates in support of their academic qualifications.

6. Applicants in employment (private, government or any other organization) are required to submit a “No Objection Certificate” from the employer at the time of interview.

7. No TA/DA will be paid for appearing in the interview.

Age Limit: 27 years as on the last date of application (The upper age limit is relaxable upto 5 years in the case of candidates belonging to SC/ST/OBC/PH and women candidates).

Name of Principal Investigator: Dr Rekha Verma

Designation: Assistant Professor

Department of Electronics and Communication Engineering



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For office use only
Application No.....

APPLICATION FORM

Advt.No.....

Dated.....

Research Project Entitled- “Silicon Nanowires: Professional Compact Model of Scaled Thermoelectric Figure of Merit for Energy Harvesting”

File No. : YSS/2015/000905

Funding Agency: Science and Engineering Research Board (SERB), New Delhi

Name of P.I.: Dr. Rekha Verma

Name of position: Junior Research Fellow (on contract)

Department/Section: Department of Electronics and Communication Engineering

1. Name of the Candidate:

(In Capital letter)

2. Sex (Male/Female):

3. Marital Status (Married/Unmarried):

4. Date of Birth:

5. Age (as on last date of application): _____ Years _____ Months _____ Days

6. Father's/Husband's Name:

7. Present/Contact Address:

8. Permanent Address:

9. Mobile No:

10. E-mail:

11. Nationality:

12. Category Gen/SC/ST/OBC/PH:

13. GATE/NET/JEST Valid up to: _____ Rank _____

14. Educational Qualification (10th onward):

Qualification	Subject/Discipline	Board/Institute/Univ.	Year of passing	% of marks	Division
10th					
12th					
Diploma, if any					

B.Sc./B.E./B.Tech /M.Sc. or equivalent					
M.E./M.Tech*					

***Title of M.E/M.Tech Thesis:**

Supervisor's name:

15. Details of Experience in chronological order. Enclose a separate sheet duly authenticated under your signature if necessary

16. Publications details (Attach a separate list):

17. Any other information:

18. Indicate whether you propose to register for higher degree* at IITA, Allahabad: Yes/No
(Name of higher degree is Ph.D.)

Note:- *Those candidates who wish to be admitted as student for higher degree (Ph.D.) must fulfill atleast the minimum eligibility for the respective admission as available on the Institute's website.

DECLARATION

I hereby declare that all the statements made in this application are true and complete and nothing has been concealed/ distorted. I am aware that, if at any time I am found to have concealed/distorted any material information, my engagement is liable to be summarily terminated without notice.

Place:
Date:

Signature of the Applicant

Checklist-

1. Self-attested copy of documents
2. Proof of Date of Birth
3. Mark sheets & certificates of all the examination passed
4. Category Certificate, if applicable
5. GATE/NET/JEST score card, if passed
6. Experience certificate, if any