

INDIAN INSTITUTE OF INFORMATION TECHNOLOGY, ALLAHABAD

Deoghat Jhalwa, Allahabad-211012 (U.P.), India

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Advt. No. 1/SMDPC2SD/2019 Dated -17/1/2019

Walk in Interview for the Post of Research Associate (On Contract) and Lab Engineer (on contract)

Recruitment of **Research Associate (on contract)** and **Lab Engineer (on contract)** in DeitY sponsored project titled “**Special Manpower Development programme for Chips to System design (SMDP-C2SD)**”. The interested candidates may attend the interview with original and attested copies of all academic and experience certificate (if any) and testimonials on Feb 1st, 2019 at 9.30 am in the department of ECE Room No.2106, Computer Centre–1, Indian Institute of Information Technology Allahabad, Devghat, Jhalwa, Allahabad, Uttar Pradesh 211012.

S. No	No of Positions	Post	Qualification	Duration	Consolidated Salary
1	01	Research Associate	B.Tech. (ECE) Knowledge of VLSI design Tools and relevant experience in VLSI related projects will be preferred	1 year	Rs 24000/- (per month)
1	01	Lab Engineer	Btech (ECE) and MTech in the field of Microelectronics/VLSI Knowledge of VLSI design Tools and relevant experience in VLSI related projects will be preferred	1 year	Rs 30000/- (per month)

Note:

1. **The position is purely temporary and will be governed by the Funding agency rules & service conditions of IIIT Allahabad.**
2. Mere, possessing the prescribed qualification does not ensure that the candidate would be selected.
3. Applicants in employment (private, government or any other organization) are required to submit a “No Objection Certificate” from the employer at the time of interview.
4. No TA/DA will be paid for appearing in the interview.
5. Candidates have to appear for walk in interview with bio-data, passport size photo, original certificates and experience certificate (if any)

Dr. Manish Goswami
Chief Investigator
SMDP-C2SD project.
IIIT-Allahabad

Dr Prasanna Misra
Co-Chief Investigator
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