



भारतीय सूचना प्रौद्योगिकी संस्थान, इलाहाबाद Indian Institute of Information Technology, Allahabad

An Institute of National Importance by Act of Parliament
Deoghat, Jhalwa, Allahabad-211015 (U.P.) INDIA

Ph.: 0532-2922025, 2922067, Fax : 0532-2430006, Web : www.iiita.ac.in, E-mail : contact@iiita.ac.in

Ref no. IIIT-A/ENQ/Purchase/1010/1508/2022
Date: 09/03/2022

Enquiry Letter

M/s.

Ph. No.:

Sub: Quotation for board development (Design, BOM, Fabrication and assembly (2 piece) and 1 pc socket) along with single site FT Test solution development of true random number generator block in some tester platform.

Enquiry issue date : 09/03/2022

Last submission date : 23/03/2022 upto 12:00 Noon

Opening of Bid : 23/03/2022 at 4:00 PM

Dear Sir,

Institute intends to get the quotations for eligible vendors 'Board development (Design, BOM, Fabrication and assembly (2 piece) and 1 pc socket) along with single site FT Test solution development of true random number generator block in some tester platform', for which quotations are invited as per details given in below:

S.No.	Items/Description	Unit	Qty.	Unit Price	Total Amount
1.	Engineering Charges for Single Site FT Test Solution development of EDU080 product TRNG block in ETS364	Set	01		
2.	Single Site Adaptor Board Development- Design, BOM, Fabrication & Assembly (02 Pc's) and 01 pc Socket	Set	01		
Total Rs.					
Taxes (GST) Rs.					
Grand Total Rs.					

a) The scope of the work shall include the following activities:

- Product Study
- Adaptor board design and development
- Single Site ETS364 FT Test Program Code development
- Debug and screening of 05 unit

b) Deliverables will be datalog and 5 screened devices for TRNG

c) Chip Test Loadboard+DUT board will be used for debug & release and will not part of deliverable



SK

You are requested to submit the sealed quotation by courier/speed post with complete details of specifications, terms & conditions, etc. **upto 23.03.2022 at 12:00 Noon**. Quotations duly sealed may also be dropped in the tender box placed in the office of the Joint Registrar (S&P), IIIT-Allahabad. Basic rate, taxes and freight charges etc. must be quoted separately. **Kindly mention enquiry number, subject, due date, contact address etc., on your quotation. Incomplete quotation will not be accepted.**

Quotation should be addressed to **Joint Registrar (S&P), IIIT-Allahabad, Deoghat Jhalwa, Prayagraj -211015.**

Others Terms and Condition

1. FOR destination IIIT-Allahabad, Deoghat Jhalwa, Prayagraj.
2. All costs and expenses involved in supply should be included in quoted in price schedule.
3. The Company should have experience of 15 years in Chip Testing work. (Attached the documentary evidence, failing which quotation cannot be considered).
4. Quoted rate should be valid at least for 90 days from the last date of submission of tender.
5. Tenderer should submit GSTIN/PAN/Aadhar copy with quotation.
6. Enquiry/tender must be quoted in prescribed format on the letter head of the firm/vendor, otherwise quotation shall be rejected outright.
7. Supply should be completed within 05 weeks from the receipt of the purchase order, If the work delayed beyond the stipulated time of completion penalty of 1% per week or part thereof of total cost shall be imposed at the discretion of competent authority. The penalty may be upto 10% of the total accepted bid value.
8. If it is found that supplied/developed board is fake or of sub-standard the firm be liable to be blacklisted.
9. Conditional quotations are liable to be rejected. In the event of acceptance, Director's decision shall be final and binding on both the parties. The rates should be quoted as per our required specifications. The quotation which is not as per our required specifications will not be considered.
10. The right to accept or reject any tender/ quotation, partially or wholly, including lowest quotation without assigning any reason whatsoever thereof or incurring any liability thereby is reserved with the Director, IIIT, Allahabad. The decision of the Director, with regard to enforcement of these terms and conditions herein contained, as a result of breach of these conditions by successful Tenderers, shall be final and the Director, shall not be liable for any damage/liability put forth by the Tenderers at any stage of the transaction arising out of the enforcement of any or all herein contained terms and conditions.
11. The tenderer who submits false, forged or fabricated documents or conceals facts with intention to win over the tender or procure purchase order; Performance Security of such tenderer firm shall be forfeited and firm shall be liable for blacklisting for a period of not Less than 2 years. The firm shall also be liable for Legal action depending on the facts & circumstances of the case.
12. Payment will be made within fifteen days after completion of work and satisfactory report. No conditions/clause with regard to interest etc. shall be entertained.



Handwritten initials in blue ink, possibly 'SN' and 'OS'.

13. Tenderer should take into account any corrigendum published on the Tender document before submitting their bids. All such corrigendum will be placed on IIIT-A website only www.iiita.ac.in. Intending tenderers are advised to visit www.iiita.ac.in for regular updates, if any, till the closing date of tender for any corrigendum/ addendum/ amendment. IIITA will not be responsible for ignorance of corrigendum.
14. For any technical queries/clarification related to Enquiry, please contact to Prof. Manish Goswami, Indian Institute of Information Technology Allahabad, Prayagraj-211015, Uttar Pradesh Mobile: +91 9792947813, email: manishgoswami@iiita.ac.in, during office hours [10:00 to 18:00 hours]
15. The Director of IIIT-Allahabad reserves the right to reject any or all the bids, or cancel the tender/order, without assigning any reason and the decision of the Director; IIIT-Allahabad shall be final and binding.
16. Director, Indian Institute of Information Technology, Allahabad, Prayagraj will be the sole arbitrator of all the disputes and his decision will be binding on both the parties.
17. Director, Indian Institute of Information Technology, Allahabad, Prayagraj reserves the right to alter/modify any or all conditions of this tender notice.
18. For any other queries feel free to contact Purchase Section through E-mail-info.purchase@iiita.ac.in (Ph. No. : 0532-2922051) for any queries.
19. The lowest rate will not be the basis of claim to get the order.
20. All legal disputes shall be subject to Jurisdiction at courts at Prayagraj.


Joint Registrar (S&P)
Read and accepted:

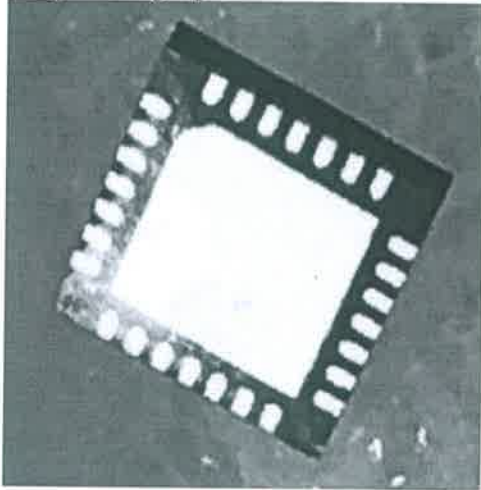

Signature & stamp of Bidder or
Authorized Signatory

Copy to:

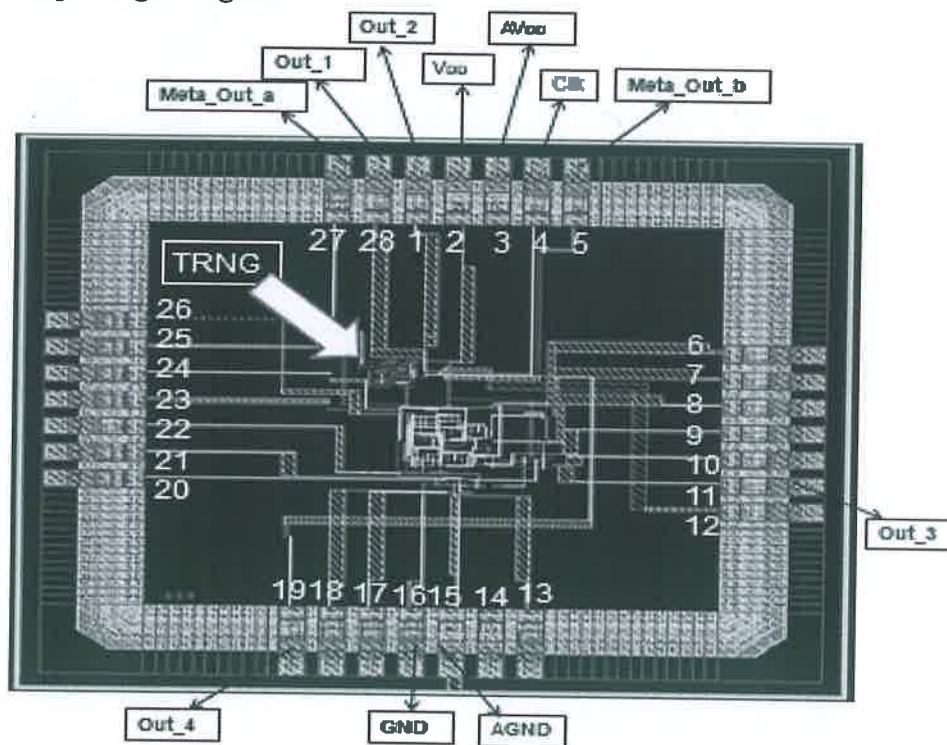
- PS to Director for kind information.

Details of Chip

Chip Photograph



Chip Design Diagram



Handwritten signature



Annexure-I

Chip Input output Details-

INPUT: VDD-Power Supply- 1.8v DC:

Pin -18

INPUT: GROUND-Power Supply- -0V :

Pin-5

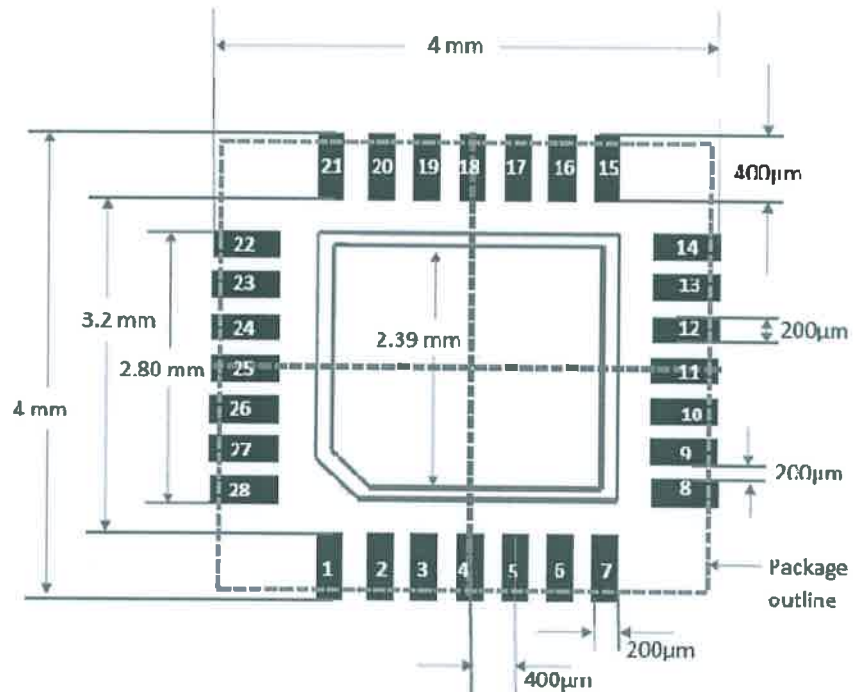
INPUT: Input for initialization:

Pin 21

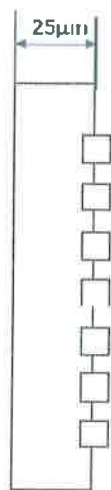
any impulse trigger source (to activate ring oscillator):

OUTPUT (For NIST & FIPS testing):

Pin-1, 8, 15, 16, 19, 20



Side view



Bottom view

